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1374.45693X00

IAP20 Rec'd PCT/PTO 29 DEC 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Chuichi MIYAZAKI et al.
Serial No.: Not Yet Assigned
Filed: December 29, 2005
For: MANUFACTURING METHOD OF SEMICONDUCTOR
INTEGRATED CIRCUIT DEVICE

INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR 1.97 & 1.98

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

December 29, 2005

Sir:

In the matter of the above-identified application, applicant(s) are submitting herewith a copy of the documents, except the U.S. patent documents, listed in the attached form equivalent to Form PTO/SB/08 for the Examiner's consideration.

This information disclosure statement is being submitted with the new application.

To the extent that the documents listed on the attached form equivalent to Form PTO/SB/08 are not in the English language, the requirement of 37 CFR 1.98(a)(3) for a concise explanation of the relevance is satisfied by an English abstract, the International Search Report and/or the discussion of these documents in the specification.

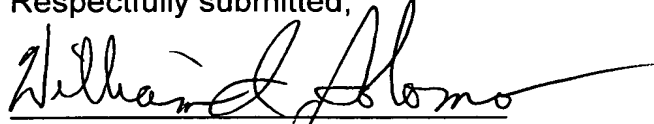
It is respectfully requested that this information disclosure statement be considered by the Examiner.

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Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account No. 01-2135 (1374.45693X00) and please credit any excess fees to such deposit account.

Respectfully submitted,

A handwritten signature in black ink, appearing to read "William I. Solomon", written over a horizontal line.

William I. Solomon
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WIS/dc
Attachments
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